

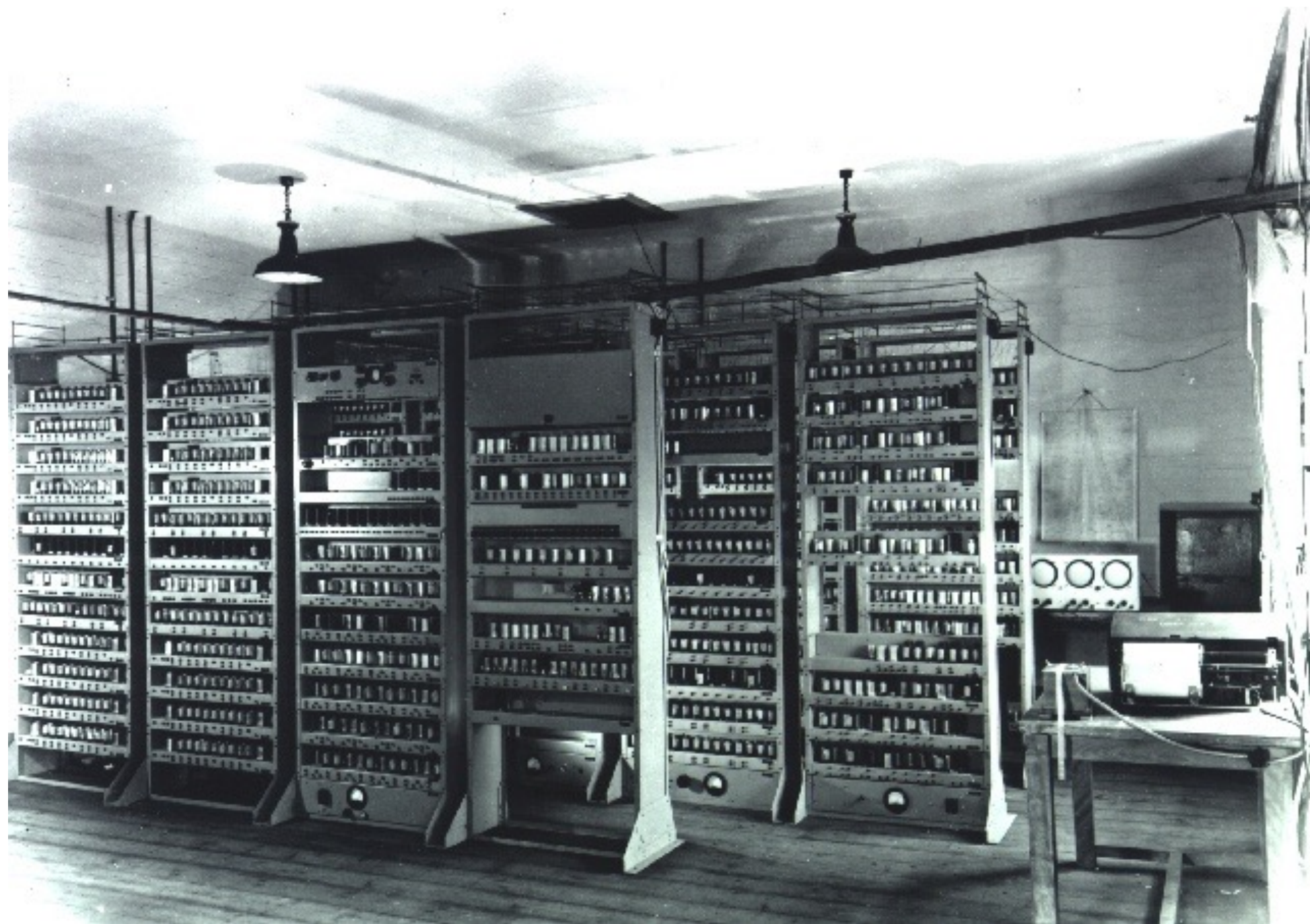
Programming the EDSAC

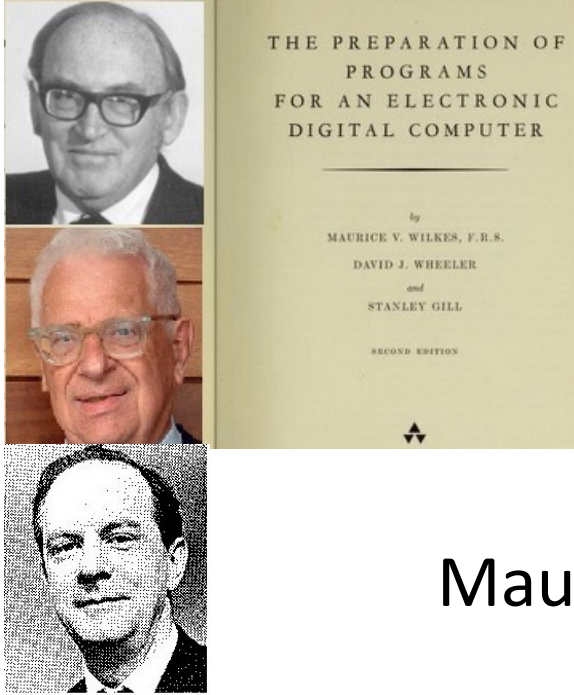


Andrew Herbert
The National Museum of Computing
18th April 2024

EDSAC FIRSTS

- The world's first PRACTICAL electronic digital stored program computer = computer of the modern kind
- The world's first computer programming system





The Preparation of Programs for an Electronic Digital Computer

Maurice .V. Wilkes, David .J. Wheeler and Stanley Gill
Addison Wesley, 1951

With special reference to the use of the EDSAC

Why a Programming System?

The methods of preparing programs for the EDSAC were developed with a view to reducing to a minimum the amount of labour required, and hence of making it feasible to use the machine for problems which require only a few hours of computing time as well as for those which require many hours. This necessitated the establishment of a library of subroutines and the development of systematic methods for constructing programs with their aid.

[WWG 1951]

Note emphasis on programmer productivity rather than on "optimal programming".

To the potential user of an automatic digital calculating machine, the successful design and construction of the machine itself is only a first step, though certainly an essential one. In order that the machine should in practice be useful to him in the calculations he may desire to carry out with its aid, the provision of an adequate organization for using the machine is as important as the machine itself.

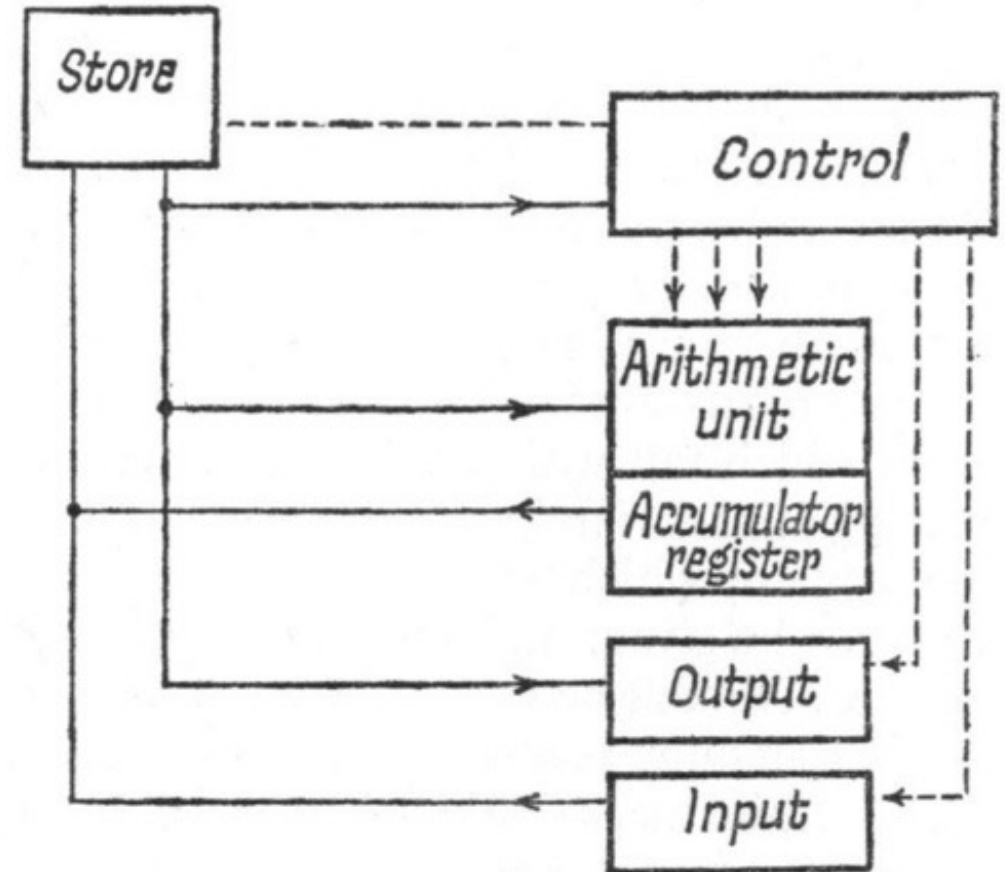
The process of building up [such] a library of subroutines, and testing its value by practical use, appears to have proceeded further at the Mathematical Laboratory of the University of Cambridge than elsewhere.

. . . it is a practical and useful system has been tested by experience; it divests programming of the appearance of being something of a magic art, closed except to a few specialists, and makes it an activity simple enough to be undertaken by the potential user who has not the opportunity to give his whole time to the subject.

The subject is one which is still developing. . .

1. Store: ultrasonic delay line holding 1024×17 binary digit numbers stored in true two's complement form, so most significant digit corresponds to the sign of the number.
2. Numbers are held in 1024 numbered "storage locations" numbered serially from 0 to 1023 for reference. Each such reference number is often called the "address" of the associated storage location.
3. Hence 17-bit numbers are often called "short numbers"
4. Two consecutive storage locations, starting from an even numbered address can be combined to make a 35-bit "long number".
5. Arithmetic unit: add, complement, collate, shift. Combine to enable subtract, multiply, round, but no divide.
6. Accumulator register of 71 bits.
7. Multiplier register of 35 bits.
8. Input: 5 hole paper tape read by photoelectric reader.
9. Output: teleprinter.
10. Control: an "order" passes from store into the control unit ("Stage I"), then it is executed ("Stage II"). The machine then, generally, then automatically takes the next order from the location following that of the order just executed.

EDSAC Block Diagram



Order Code: O A F or O A D

O: Function code – operation to be carried out

A: Address of location to be used as operand

F or D: Short or Long number

A n	Add C(n) to Acc	L 2^{p-2} F	Multiply by 2^p ($2 < p \leq 12$)
S n	Subtract C(n) from Acc	R 2^{p-2} F	Multiply by 2^{-p} ($2 < p \leq 12$)
H n	Copy C(n) to Multiplier	E n F	If C(Acc) ≥ 0 execute next order from location n; otherwise proceed serially
V n	Multiply C(n) by C(Mult) and add product to Acc	G n F	If C(Acc) < 0 execute next order from location n; otherwise proceed serially
N n	Multiply C(n) by C(Mult) and subtract product from Acc	I n F	Read next 5 bit code from input to location n from tape reader
T n	Transfer C(Acc) to location n and clear Acc	O n F	Print character set up on teleprinter, then set up m.s. 5 bits of location n as next character
U n	Transfer C(Acc) to location n but do not clear Acc	F	Read back last set character
C n	Collate C(n) with C(Mult) and add to Acc	X	Ineffective – no-op
R D	Right shift Acc one place ($\times 2^{-1}$)	Y	Round Acc to 34 digits (i.e., add 2^{-35}).
L D	left shift Acc one place ($\times 2$)	Z	Halt and ring the bell

Fixed Point Arithmetic

Binary point assumed between top two most significant bits, so numbers are $-1 \leq x < 1$.

Thus, A order computes $x+y-2$ if $x+y \geq 1$, and $x+y+2$ if $x+y < -1$.

When two long numbers are multiplied together the resulting 71 digits are available in the Accumulator.

$$0.5 + 0.25 + (0.5 * 0.25) = 0.875$$

Compute $x+y+xy$; x in location 6, y in location 7.

- (0) T 8 F ; clear Acc
- (1) A 6 F ; add x
- (2) A 7 F ; add y
- (3) H 6 F ; x to Multiplier
- (4) V 7 F ; add $x*y$ to Acc
- (5) Z 0 F ; halt
- (6) +0.5 ; 01000000000000000000 x
- (7) +0.25 ; 00100000000000000000 y
- (8) (spare)

Demo1

Integer Arithmetic

Can treat accumulator as holding integers for A, S, C, L, R, E, G but for N, V multiplier is always treated as a fraction.

i.e., integers are stored as value * 2^{-16} so need to multiply by 2^{16} after multiplication.

$$10 + 5 + (10 * 5) = 65$$

Compute $x+y+xy$, x in location 8, y in location 9.

- (0) T 10 F ; clear Acc
- (1) H 8 F ; x to Multiplier
- (2) V 9 F ; form $x*y$ in Acc
- (3) L 512 F ; multiply by $2^{16} = 2^{11} * 2^5$
- (4) L 8 F
- (5) A 8 F ; add x – n.b. after multiply
- (6) A 9 F ; add y
- (7) Z 0 F ; halt
- (8) +10 ; 0000000000000001010 x
- (9) +5 ; 0000000000000000101 y
- (10) (spare)

Demo2

Loops

- Loop to print digit 7 five times
- C(11) is “figure shift”
- C(12) is “7”
- C(13) is RET
- Loops while Acc < 0 (-5, -4, -3, -2, -1)
- Note: need to set figure vs. letter shift
- Note: output delayed one character
- Note: only G (< 0) and E (>= 0), but no “equals” order

(0)	T14F	; Clear Acc
(1)	O10F	; Type figure shift
(2)	S9F	; Set count = -5
(3)*	A10F	; Increment count
(4)	O12F	; Type '7'
(5)	G3F	; jump back to * if count < 0
(6)	O13F	; Type RET
(7)	O11F	; Type figure shift
(8)	Z0F	; Halt
(9)	+5	
(10)	+1	
(11)	010110000000000000	; fig shift
(12)	001110000000000000	; +7
(13)	110000000000000000	; RET
(14)	(spare)	

Demo3

EDSAC character codes

Programs prepared on perforator

Note NO figure / number shift

for program or data input!

Output produced in Teleprinter code

Order field in instruction is the bit pattern of the order character, i.e., A = 11101

Note conventions for typing Greek letters when using emulators

Table 2 Edsac Character Codes

Perforator		Teleprinter		Binary	Decimal
Letter shift	Figure shift	Letter shift	Figure shift		
P	0	P	0	00000	0
Q	1	Q	1	00001	1
W	2	W	2	00010	2
E	3	E	3	00011	3
R	4	R	4	00100	4
T	5	T	5	00101	5
Y	6	Y	6	00110	6
U	7	U	7	00111	7
I	8	I	8	01000	8
O	9	O	9	01001	9
J		J		01010	10
π		Figure Shift		01011	11
S		S	"	01100	12
Z		Z	+	01101	13
K		K	(01110	14
Erase ¹		Letter Shift		01111	15
Blank tape ²		(no effect)		10000	16
F		F	\$	10001	17
θ		Carriage Return		10010	18
D		D	;	10011	19
ϕ		Space		10100	20
H	+	H	£	10101	21
N	-	N	,	10110	22
M		M	.	10111	23
Δ		Line Feed		11000	24
L		L)	11001	25
X		X	/	11010	26
G		G	#	11011	27
A		A	-	11100	28
B		B	?	11101	29
C		C	:	11110	30
V		V	=	11111	31

Notes

¹ Erase is represented by an asterisk ("*") in the simulator. When this character is *output*, it sets the teleprinter into letter shift.

² Blank tape is represented by a period ("."). This character has no effect on output.

³ The personal computer text environment has only a "newline" character. On the Edsac simulator, the line-feed character is interpreted as a newline character, and carriage returns are thrown away.

⁴ The symbols θ , ϕ , Δ or π are typed as @, !, & and #, respectively.

Indexing

• Initially EDSAC had no index register.	(0)	T13F	; Clear acc
• Invented for Manchester Mark 1 in 1949.	(1)	A14F	; Pick up sum so far
• Later adopted by EDSAC.	(2)**	A15F	; Add vector[0]
	(3)	T14F	; Store in sum
• So to do an indexed calculation, e.g., sum a vector, we have to write self-modifying code that manipulates program in store.	(4)	A2F	; C(2)
	(5)	A12F	; C(2)+2 (inc. address)
	(6)	U2F	; Modify (2) **
• To do arithmetic on orders we need to understand binary format of orders:	(7)	S20F	; Check for sentinel
• 5 m.s. bits: order code	(8)	G0F	; Loop if not at end
• 1 bit : spare (later add B register to address)	(9)	T13F	; Clear acc
• 10 bits: address in range 0-1023	(10)	A14F	; Result
• l.s. bit: 0 = F, 1 = D	(11)	Z0F	; Halt
• Demo adds contents of vector starting at location 15	(12)	+2	; address stride
	(13)	+0	; workspace
	(14)	+0	; sum
• We have to add +2 to location 2 each time around the loop to fetch the next element of table	(15-19)	+1 +2 +3 +4 +5	; vector
	(20)	A20F	; sentinel (Demo4)

- Demos thus far run using EDSAC team test program generator:
 - Assembler written with “modern” facilities
 - Emulator written in C with tracing facilities
 - adapted from original by Lee Wittenberg
 - On EDSAC we use a “Signal Sequence Injector” box to set up program in main store from location 0 onwards
- Visit GitHub/andrewjherbert to find these:
 - edsacasm - <https://github.com/andrewjherbert/edsacasm> - Python
 - edsac - <https://github.com/andrewjherbert/EDSAC-Emulator> - C
- But this is not how EDSAC users wrote code...

EDSAC PROGRAMME SHEET

REF

DATE

Calculation of curves for $y = \frac{1}{2} \sin x$.Calculates $\frac{1}{2}$, $\frac{1}{128 \times 8}$, $\frac{1}{64 \times 8}$ Use with tape WSG:

	Order	Notes		Order	Notes
0	P F		0	V 2047 D	Starts at $\sin^2 D$
1	T 134 K		1	K 4095 D	$a = 2 (6 \times 10^{-10})$
2	P x F	$x = 48 \times 2048$	2	P h (F.D)	$h = \sin^2 D \times 32768$
3	T 126 K	F in 126	3	P F	for starting value.
4	P y F	$y = \frac{2048}{126} F_0$	4	T 136 K	
5	T 294 K		5	P F	clears 2
6	E 231 F		6	P F	
7	T 231 K		7	T 358 K	
8	A 231 F		8	A 243 F	
9	Q 165 F		9	T 126 K	
0	A D		0	T 323 K	
1	T 288 D		1	T 171 K	
2	A 235 F		2	E 179 F	
3	Q 91 F		3		
4	A 288 D		4	T 179 K	
5	T D		5	A 126 D	
6	O 241 F		6	T 132 K	
7	E 296 F		7	P 10813 F	
8	Q F		8	P 32000 F	
9	W F	\rightarrow (change to P & F value $h = \sin^2 D \times 32768$ into 2048 in all steps)	9	T 317 K	
0	P 322 F	$\Delta F \times 2048 = g(x, F_0)$	0	P 256 F	O 241 F
1	T 329 K		1	T 314 K	T D
2	A 126 D		2	T 36 D	E 316 F
3	A 243 F		3	E 248 F	T 366 K
4	T 126 D	$\{ n = \text{no of steps before next value of } \sin^2 D$	4	T 248 K	P n F
5	T 355 K		5	H 36 D	T 211 K } $\text{for } D \text{ only.}$
6	A 128 D		6	V 288 D	S
7	A 242 F		7	T D	
8	T 128 D		8	A 251 F	E 144 K
9	T 128 K.		9	Q 91 F	P F.

#1

TITLE WRITTEN
ON TAPE

PF
T 134 K
P 1024 F
T 242 K
C 143
T 126 K
P..F
P..F
T 370 K
P..F
T 171 K
T 146 D
E 378 F
T 278 K
H 384 F
N 146 D

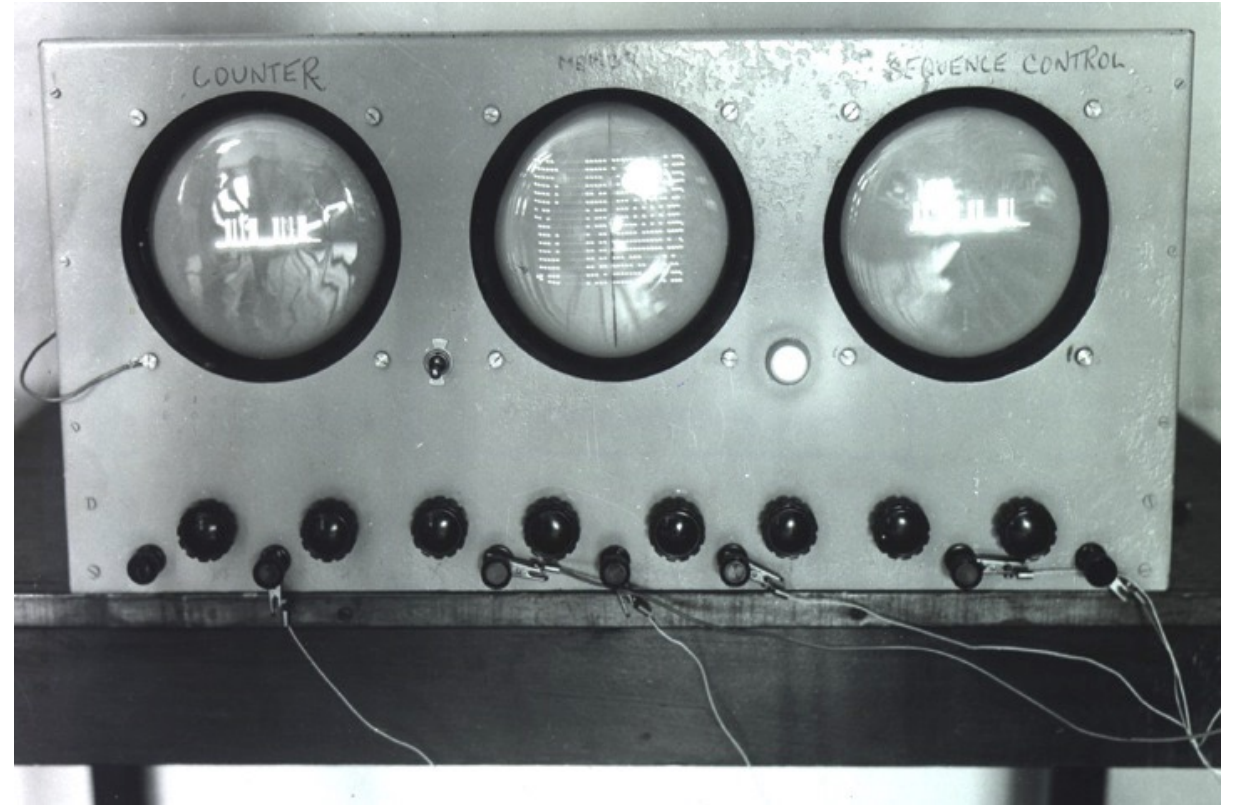
T 144 D

E 181 D

E 144 K

PF





031164 +019084
 +032194 +018876
 +033509 +018732
 +035237 +018687
 +037588 +018794
 +040919 +019138
 +045875 +019864
 +053687 +021222
 +066909 +023656
 +091337 +027966
 +141844 +035461
 +268904 +047536

$$\beta = \frac{7}{16}$$

$$\delta y = \frac{3}{56}$$

β

$$\beta = \frac{7}{16}$$

$$\delta y = \frac{3}{56}$$

+031130 +019063
 +032182 +018868
 +033528 +018743
 +035304 +018723
 +037726 +018863
 +041163 +019252
 +046278 +020039
 +054331 +021476
 +067907 +024009
 +092799 +028414
 +143687 +035922
 +270334 +047789

1.05357..

+031106 +019048
 +032179 +018867
 +033558 +018759
 +035380 +018763
 +037871 +018936
 +041412 +019369
 +046680 +020213
 +054959 +021724
 +068860 +024346
 +094165 +028832
 +145354 +036339
 +271576 +048008

1.0714...

+031091 +019039
 +032185 +018870

Initial Orders

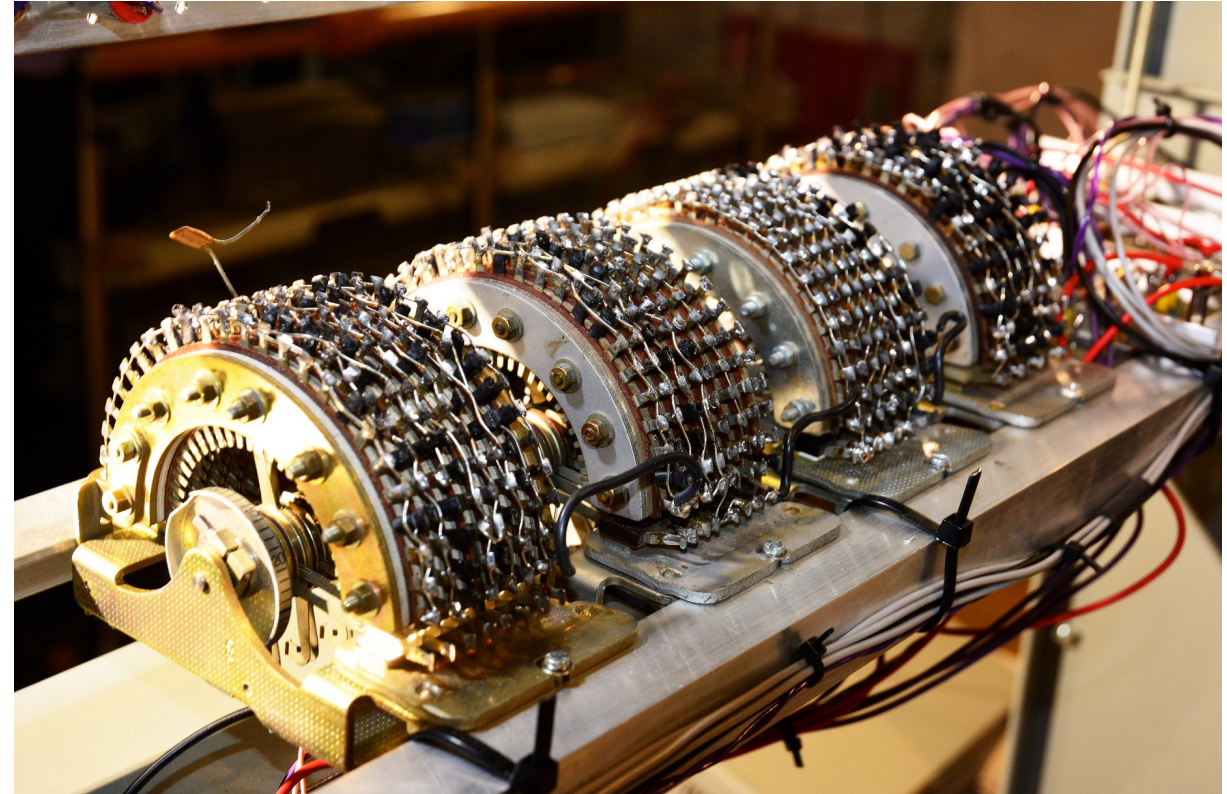
Fixed program to load source programs from paper tape into store

Input is alphanumeric

Combined assembler and linker to enable user code to be linked to predefined library routines

Unique to EDSAC

Programming tour de force by David Wheeler



Proc. Royal Society A, 202, August 1950: D.J. Wheeler,
Programme organization and initial orders for the EDSAC.
<https://royalsocietypublishing.org/doi/10.1098/rspa.1950.0121>

Initial orders concepts

- Instructions in alphanumeric form rather than binary
 - Like modern assembly code
- Control codes to direct initial orders where to load and how to fix up addresses, start execution
 - To enable linking in subroutines in arbitrary order
- Addressing relative to a previously set parameter (control code)
- But no error handling!

Warwick Simulator

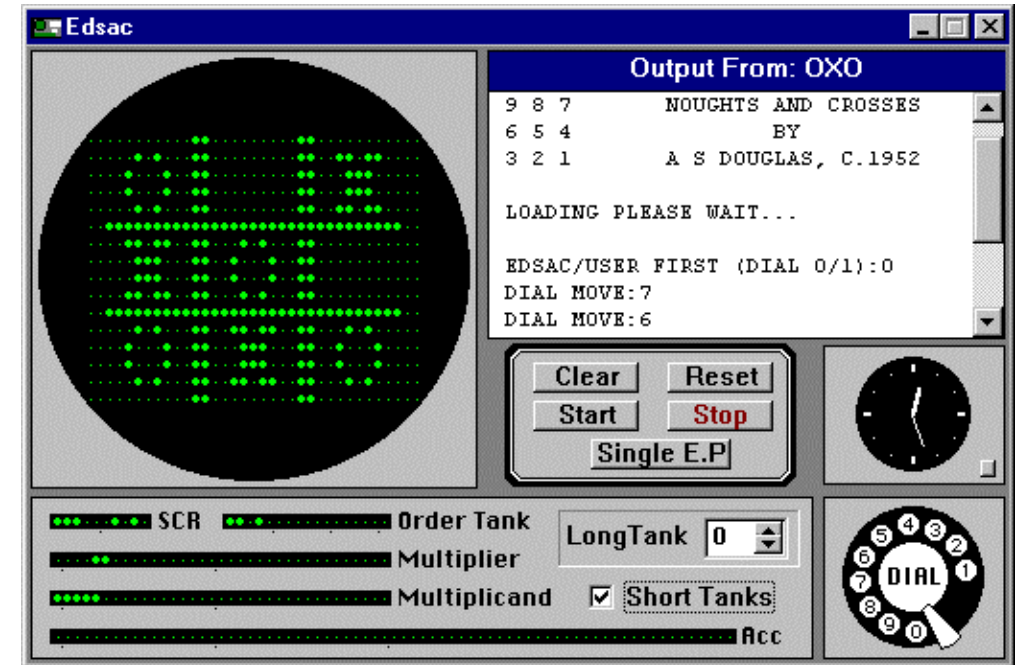
Written by Martin Campbell-Kelly

Available for Windows and MacOS

GUI replicates original EDSAC operation

<https://edsac.net>

Includes Tutorial Guide, original EDSAC subroutine library, worked example programs



Hello world

Start → 0

T 64 K G K and E Z P F are control combinations

θ is a “parametric address”

T 64 K – load from loc 64

G K – set θ (to 64)

E Z P F – enter program at Location θ (64)

* Is erase character (32 decimal)

T	64	K	Load from location 64
G		K	Set θ parameter
Z		F	Stop
1	O	5 θ	Print letter shift
2	O	6 θ	Print "H"
3	O	7 θ	Print "I"
4	Z	F	Stop
5	*	F	Letter shift
6	H	F	"H"
7	I	F	"I"
	E	Z	Enter at location 00
	P	F	

(a) Program text

T64K

GK

ZF

O5@

O6@

O7@

ZF

*F

HF

IF

EZPF

(b) Program tape

N.B. Data input as instructions

Control combinations

T m K	set load point to m
G K	set θ parameter to load point
T Z	restore θ parameter
E m K P F	enter program at location m
E Z P F	enter program at location θ
P Z or P K	start of new tape block

Subroutines – the Wheeler jump

A m F to pick up where calling
from (m+1)

A m F is 11000... so negative

$C(3) = U\ 2\ F$

Calculate E m+2 F and store as
final instruction

Return to caller

m	A	m	F	pick up self	master routine
m+1	G	n	F	jump to subroutine	
m+2	.			control returns here	
	.				
	.				
n	A	3	F	form return link	subroutine
n+1	T	p	F	plant return link	
	.				
	.				
p	(.)	return link planted here	

Cubes

Nichomachus' formula for cubes:

$$1^3 = 1$$

$$2^3 = 3 + 5$$

$$3^3 = 7 + 9 + 11$$

$$4^3 = 13 + 15 + 17 + 19$$

etc

Use library routine P6 to print integers

.. represents blank tape

Enter→	0	G	K	Set 0-parameter
	1	Z	F	Stop
	2	O	29 0	Figure shift
22 →	3	O	30 0	New line
	4	O	31 0	
	5	A	23 0	k to 0F
	6	T	F	
	7	A	6 0	Print 0F using P6
		G	56 F	
P6 →	8	T	23 0	Zero to k
	9	A	24 0	
	10	A	27 0	n+1 to n
	11	T	24 0	
	12	S	24 0	-n to count
21 →	13	T	26 0	
	14	A	25 0	
	15	A	28 0	m+2 to m
	16	U	25 0	
	17	A	23 0	k+m to k
	18	T	23 0	
	19	A	26 0	Increment count
	20	A	27 0	
	21	G	13 0	Jump to 13 if count • 0
	22	E	2 0	Repeat main cycle
	23	P	D	k (n ³ ; =1 initially)
	24	P	D	n (=1 initially)
	25	P	D	m (=1 initially)
	26	P	F	count
	27	P	D	=1
	28	P	1 F	=2
	29	π	F	figs
	30	θ	F	cr
	31	Δ	F	lf

(a) Master routine

Routine	Location of first order	Number of storage locations occupied
P6 (print)	56	32
Master	88	-

(b) Table of routines

space P K

T 56 K

P6

space P Z

Master

E Z P F

(c) Make-up of program tape

1
8
27
64
125
216
343
512
.
.
.

(e) Printout

[Cubes]

```
..PK
T56K
[P6]
GKA3FT25@H29@VFT4DA3@TFH30@S6@T1F
V4DU4DAFG26@TFTFO5FA4DF4FS4F
L4FT4DA1FS3@G9@EFSFO31@E20@J995FJF!F
..PZ
```

[Cubes Master]

```
GK
ZF
O29@
O30@
O31@
A23@
TF
A6@
G56F
T23@
A24@
A27@
T24@
S24@
T26@
A25@
A28@
U25@
A23@
T23@
A26@
A27@
G13@
E2@
PD
PD
PD
PF
PD
P1F
#F
@F
&F
EZPF
```

(d) Program tape

Notes

Conventional “coding sheet” style for writing programs

No layout on EDSAC tape

No comments on EDSAC tape

Use of θ to make code position independent

Constants written as pseudo-orders

Need to know length of standard subroutines (included in WWG!)

Advanced features

Code letters (terminate address field of an order)

<i>Code-letter</i>	<i>Location</i>	<i>Value</i>
F	41	0
θ	42	Origin of current routine
D	43	1
φ, H, N, M ... V	44, 45, 46 ... 55	For use by programmer

Used to create position independent code and data cross references

Subroutine parameters:

Pass via fixed address (often 0)

Include in calling sequence

Run and delete open subroutines on the fly to save store...

		G	K	
		T	47 K	Set M parameter
		P	21 0	
		T	Z	
19 →	0	S	1 M	Set count to -9
	1	T	6 M	
	2	A	2 M	$1 \cdot 2^{-4}$ to 0D
	3	T	D	
	4	A	7 M	$n \cdot 2^{-4}$ to 0F
	5	T	4 D	
	6	A	6 0	Set 0D to 0D/4D (ie. $1/n$)
	7	G	56 F	using subroutine D6
D6 →	8	O	3 M	Output new line
	9	O	4 M	
	10	O	5 M	Output decimal point
	11	A	11 0	Print 0D
	12	G	92 F	using subroutine P1
P1 →	13	P	10 F	Parameter for P1
	14	A	7 M	
	15	A	2 M	Increment n
	16	T	7 M	
	17	A	6 M	Increment
	18	A	M	and test counter
	19	G	1 0	
	20	Z	F	Stop
M	0	P	D	= 1
	1	P	4 D	= 9
	2	Q	F	= $1 \cdot 2^{-4}$
	3	0	F	carriage return
	4	Δ	F	line feed
	5	M	F	decimal point
	6	P	F	count
	7	W	F	= n ($= 2 \cdot 2^{-4}$ initially)

(a) Master routine

space P K

T 56 K

Load at loc 56

M3

Print caption

 $\theta \Delta \cdot \text{RECIPROCAL} S \theta \Delta \pi$

Table heading

space P Z

T 56 K

Load at loc 56

D6

Divide subroutine

space P Z

P1

Print subroutine

space P Z

Master

Main program

E Z P F

Enter main program

(c) Make-up of program tape

Routine	Location of first order	Number of storage locations occupied
D6 (divide)	56	36
P1 (print)	92	21
Master	113	-

(b) Table of routines

RECIPROCAL S

.4999999999

.3333333333

.2499999999

.2000000000

.1666666666

.1428571428

.1249999999

.1111111111

.0999999999

(d) Printout

Using command line emulator

Demo5

punch – convert ASCII to EDSAC code

same conventions as Warwick emulator for special symbols etc

edsac – run emulator taking input from stdin

-v1/-v2 tracing

-lⁿⁿⁿ order limit

-s to start

-b for EDSAC replica SSI emulation

tprint – convert Teleprinter output to UTF

- Debugging – post-mortem

Start reciprocals

. . . Executes . . .

Start PM5

Dial start location, e.g., 134 ($113+21$ = start of data)

Debugging – Checking (i.e., tracing)

Assemble program with checking routine at end

C7 – execution trace

·
·
·
space P Z

Master

space P Z

G K T 45 K P F

P 113 F

PNΔθPN

C7

C10 – arithmetical trace

·
·
·
space P Z

Master

space P Z

GKT45KP37θP10F

P 113 F

PNΔθPN

C10

E 113 K P F

By contrast...

From Turing's programming
guide for Manchester Mark 1

MANCHESTER UNIVERSITY ELECTRONIC COMPUTER.	
Programme Sheet 2.	
ROUTINE INPUT (SHEET 1).	
1/ E / / A / @ / E	@ R / / : A E K / A
2/ E / / T / J P	V K / / A A E E
3/ E / / E / /	Y : / / A A E E
4/ E / / A " E K T B	V K / / A A E E
5/ E / / S I V K T /	V K / / H O I A E E
6/ E / / U Z M K / C N	D S Y O I A A E E
7/ E / / 2 M K T A	C C @ T : U A A E E
8/ E / / D R V K : E	B B @ T : P A A E E
9/ Y @ / : N F V K / C N	M M K T P P R L L @
10/ / E T : E C V K P O	U @ / E @ J L L @
11/ / E T : E C V K P O	V / S T A P F L L @
12/ X @ / : E T T / X E	M K T G C L L @
13/ / E T : E T T / X E	J K S Y T T L L @
14/ L E T T E L / /	/ / H A Z L L @
15/ / E T T E L W H K T	/ / J S Y O W L L @
16/ / E T T E L H N S / P	/ A / P H L L @
17/ O E T T E Y P / D H B	/ E / : Y P F L L @
18/ Q @ T T E Q Q A / @	J J @ : P Q L L @
19/ / E T T E O B Q A /	Q @ / : Q L L @
20/ / E T T E G N S / P	U " E E T / E B L L @
21/ B @ T T E M R V : A	G G @ / P M X L L @
22/ / E T T E V V : C	J E @ E K X L L @
23/ (34/ E T : E V K / N	/ / / E /

Tape:- INPUT ONE SPECIAL INPUT TWO SPECIAL.

Fig. 2. (continued).

How to get started

- Download Warwick simulator, work through examples
- Pitfalls:
 - Remember the store is tiny
 - Be careful about long versus short numbers.
 - Remember to scale calculations.
 - Remember no index registers so vectors, arrays and stacks tedious to manipulate – consider writing subroutines / interpreters
 - Read library subroutine specifications carefully to understand parameter passing conventions and any special control combinations to load them.
 - Use code letters to divide code and data into short blocks to avoid having to renumber addresses if additional code or data inserted (or deleted).
 - Beware miscoding pseudo-orders (i.e., constants)
 - Must use library routines (R series) to input long numbers – N.B., R2 will input long integers at load time
 - Remember need to set teleprinter shift and to force out last character